



## **Shri Ramswaroop Memorial College of Engineering & Management** **Department of Electrical Engineering**

SRMGPC/EE/ION/PAC/2021-22/04

11<sup>th</sup> Feb., 2022

### Minutes of Meeting

The Programme Assessment Committee (PAC)/12<sup>th</sup> BoS meeting for the Department of Electrical and Electrical & Electronics Engineering were held in hybrid mode. The details of PAC meeting are:

Date of Meeting	Monday, 9 <sup>th</sup> Feb 2022	Timing	12:30 pm– 1:30 pm
Chaired By	Dr. K.M. Siddiqui, HoD	Co-ordinated by	Er. Jayati Vaish
Attendees	PAC committee members		

1. The following BOS members were present in the meeting.

- a. Dr. Khadim Moin Siddiqui -- Convener  
Associate Professor & Head,  
Electrical Engineering Department,  
SRMCEM, Lucknow.
- b. Er. Jayati Srivastava -- Member Secretary  
Assistant Professor,  
Electrical Engineering Department,  
SRMCEM, Lucknow.
- c. Dr. S. Chatterji -- External Expert  
Prof. (R),  
Department of Electrical Engineering  
NITTTR, Chandigarh
- d. Dr. Vivek Kumar Singh -- Special Invitee  
Research Fellow,  
Queen's University Belfast, UK.
- e. Prof. (Dr.) Indu Prabha Singh -- Special Invitee  
Associate Director & Head  
Dept. of Electronics and Communication Engg.  
SRMCEM, Lucknow
- f. Prof. K. S. Singh -- Industry Expert  
Section Engineer (R),  
UPPCL, Lucknow.

g. Dr. Anurag Dwivedi  
Assistant Professor & Head,  
Department of Electrical Engineering  
BIET, Lucknow

-- Alumni

h. Er. Sandeep Verma  
Entrepreneur, Horticulture, Lucknow

-- Alumni

2. The Convener, PAC Electrical Engineering welcomed and introduced external member of Programme Assessment Committee and thanked him for sparing his valuable time to attend the meeting.
3. The Member Secretary, PAC Electrical Engineering welcomed and introduced Special Invitee and other members of Programme Assessment Committee and thanked them for sparing their valuable time to attend the meeting.
4. The Member Secretary, PAC Electrical Engineering, with the permission of the Convener, presented the agenda of Programme Assessment Committee.
5. A Programme Assessment Committee (PAC) of EE/EN Dept. for this semester discussed the following agendas:

**Agenda 1: Confirmation of the Minutes of the 11<sup>th</sup> Board of Studies in the Department of EN & EE held on 25<sup>th</sup> November 2021 and Action Taken Report of the 11<sup>th</sup> meeting of PAC/BOS .**

**Suggestions:** The Minutes and Action Taken Report of the 11<sup>th</sup> Board of Studies meeting held on 25<sup>th</sup> November 2021 were communicated to the members. The BoS members approved the action taken report of 11<sup>th</sup> Board of Studies meeting.

**Agenda 2: Presentation of Curriculum and Review of AKTU Syllabus for Subjects existing in Even Semester 2021-22.**

**Suggestions:** The member secretary presents the list and syllabus of subjects existing in Even Semester 2021-22. PAC/BoS resolve to include beyond the syllabus content in course plan.

**Agenda 3: Network analysis and Synthesis (KEE-403)**

1. Active filters must be there in syllabus with more detail.
2. Filtering should be shown in LAB with proper simulation.

**Suggestions:** Active Filters and Filtration should be shown using simulation or CRO in Network Analysis & Synthesis.

**Agenda 4: Digital Electronics (KEE-401)**

1. FPGA (Field Programmable Gate Array) and Logic families should be in more detail.
2. Electrical students don't have background of Logic families i.e. Analog Electronics.

**Suggestions:** FPGA (Field Programmable Gate Array) and Logic families should be in more detail in AKTU syllabus for Digital Electronics (KEE-401) subject.

**Agenda 5: Microprocessor and Microcontroller (KEE-602)**

1. Syllabus has become lengthy due to inclusion of micro-controller.
2. A separate micro-controller subject should be taught to students for depth knowledge as per industry requirements.

**Suggestions:** Micro-Controller should be included as a separate subject in Microprocessor & Microcontroller (KEE- 602) subject.

**Agenda 6: Power System and Analysis (KEE-601)**

1. Stability analysis of double circuit lines should also be included.
2. For industry and professional requirements, open circuit faults and its precautions should be included in Power System-II (KEE-601).



**Suggestions:** Stability analysis of double circuit lines should also be included in Power System –II (KEE-601). For Stability analysis purpose double circuit lines can be treated as two single circuit lines.

**Agenda 7: Power Electronics (KEE-603)**

Seminar on Matrix Converter and MLI (Multi-Level Inverter).

**Suggestions:** One-week training program can be included.

**Agenda 8:** Final Year projects should be for the benefits of human race.

**Suggestions:** Small products of Sponsored Projects from small industries must be included.


**Agenda 9:** Research related subjects or NPTEL courses on Artificial Intelligence (AI) and Machine Learning (ML).

**Suggestions:** Swayam or NPTEL courses or one-week training program can be included.

**Agenda 10: Other identified gaps**

1. Guided industrial visit must be adopted where the faculty must prepare a checklist for students.
2. Industry based projects, Machine Learning and Big Data analysis should be included for industrial skills.

6. The member secretary conveyed vote of thanks to all the members of PAS/BOS for their contributions.

  
Dr. Khadim Moin Siddiqui  
Head of Department  
EN & EE  
SRMCEM, Lucknow

CC:

- (1) List – A: 3 (Director)
- (2) List- B: 1(Registrar)      4. (Academic Cell)



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**Department of Electrical Engineering**


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**Action Taken Report for Minutes of 12<sup>th</sup> PAC/BoS Meeting**

As per PAC committee suggestions, following action has been taken place:

1. Active Filters and Filtration will be shown using simulation or CRO, the task has been assigned to concerned faculty member and Lab Instructor.
2. PAC experts have recommended to include some more details about FPGA and Logic Families in University syllabus because it is very important for students. However, I have assigned the task to concerned faculty member and he will teach in detail. In the next semester, we will recommend certain topics to University BoS for possible inclusion.
3. The syllabus of Microprocessor and Microcontroller has become so much lengthy after inclusion of Microcontroller in detail. PAC experts feel that the Microcontroller has large number of applications in electrical engineering and in allied branches therefore microcontroller subject must be added separately in the syllabus. We will send this recommendation to University BoS.
4. Concerned faculty will teach in detail about double circuit lines however experts recommended to treat two single circuit lines as double circuit line.
5. We will make a detailed plan for implementation of agenda 8.
6. The department will organize one in-house training program related to Artificial Intelligence, Machine Learning and Big Data Analytics or will recommend students to join Swayam subjects related to mentioned topics.
7. Industrial visit has already been scheduled.

  
Dr. Khadim Moin Siddiqui  
Head of Department  
EE/EN  
SRMGPC, Lucknow

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